JAN 1 8 2005 M

Title: SYSTEM AND METHOD FOR VERIFYING THE INTEGRITY OF STORED INFORMATION WITHIN AN ELECTRONIC DEVICE

1st Named Inventor: Robert P. Hale

Application No.: 09/675,113

Docket No.: 42390P9144

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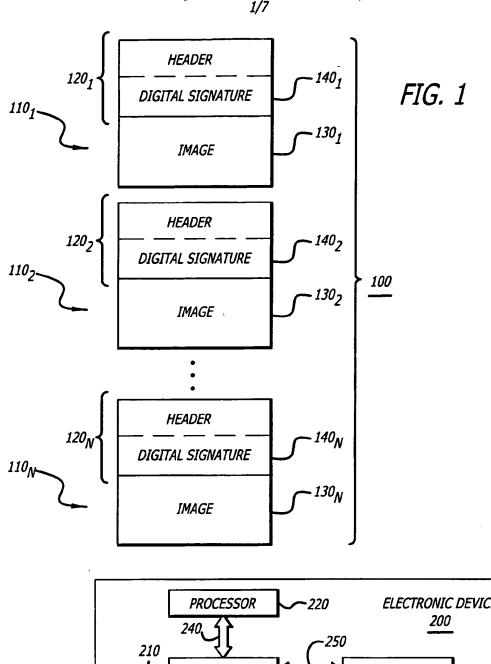


FIG. 2

PROCESSOR

220

CHIPSET

MEMORY

230

FIG. 2

PROCESSOR

250

MEMORY

230

COMMUNICATION
DEVICE

NON-VOLATILE
MEMORY

290

NON-VOLATILE
MEMORY

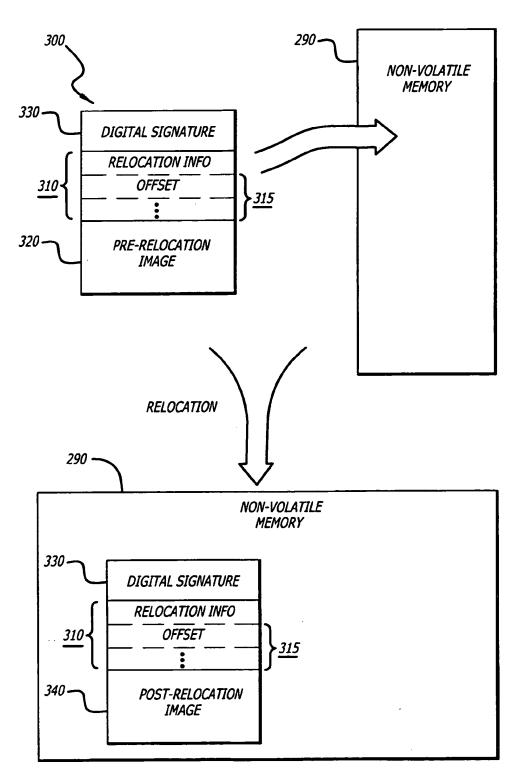
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FIG. 3

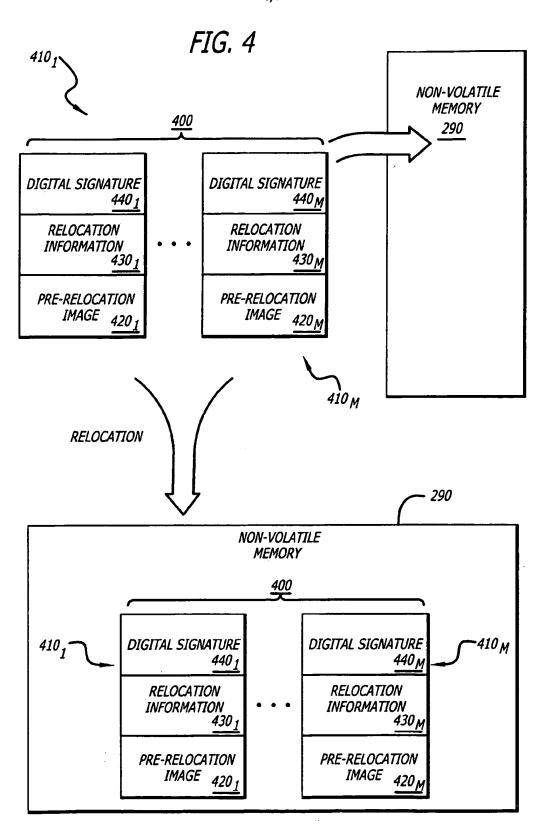


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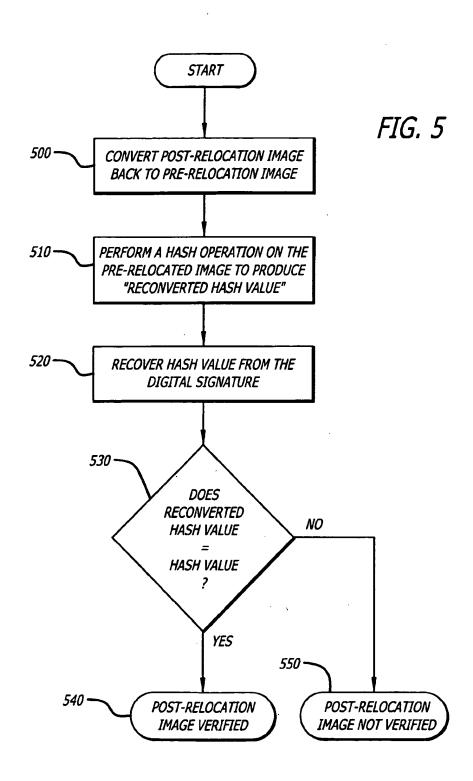


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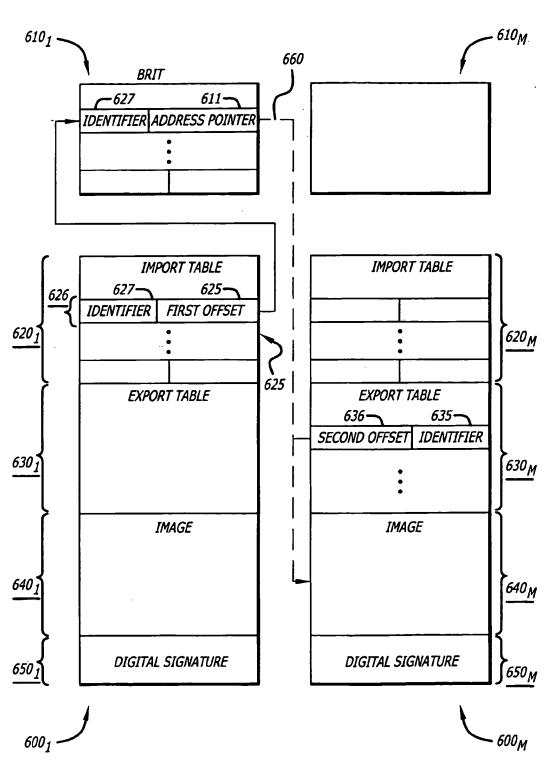


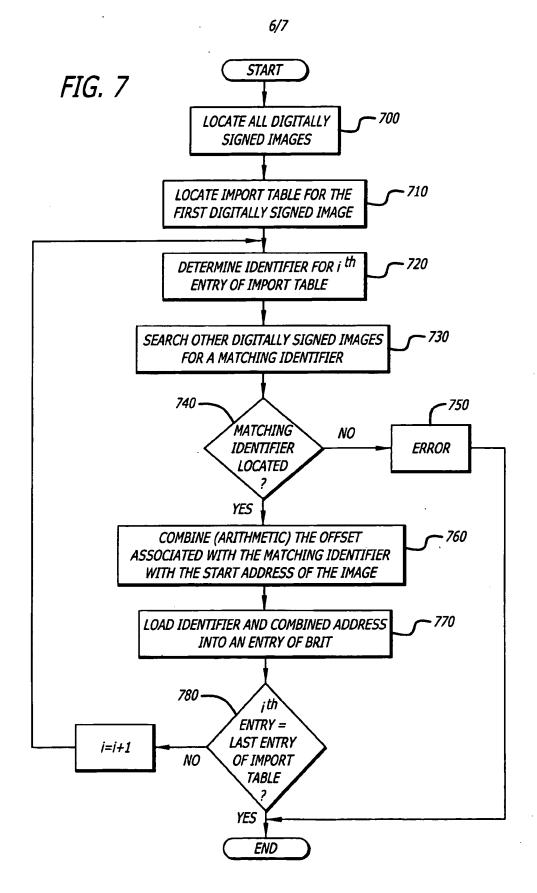
FIG. 6

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